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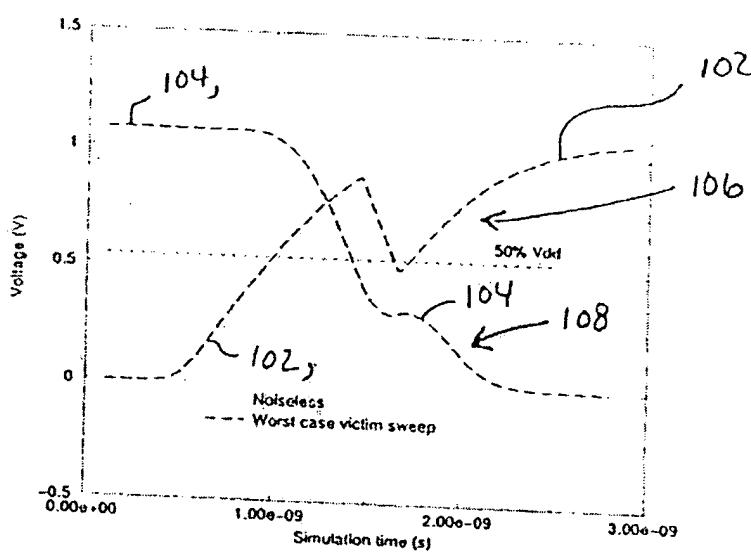


FIGURE 1

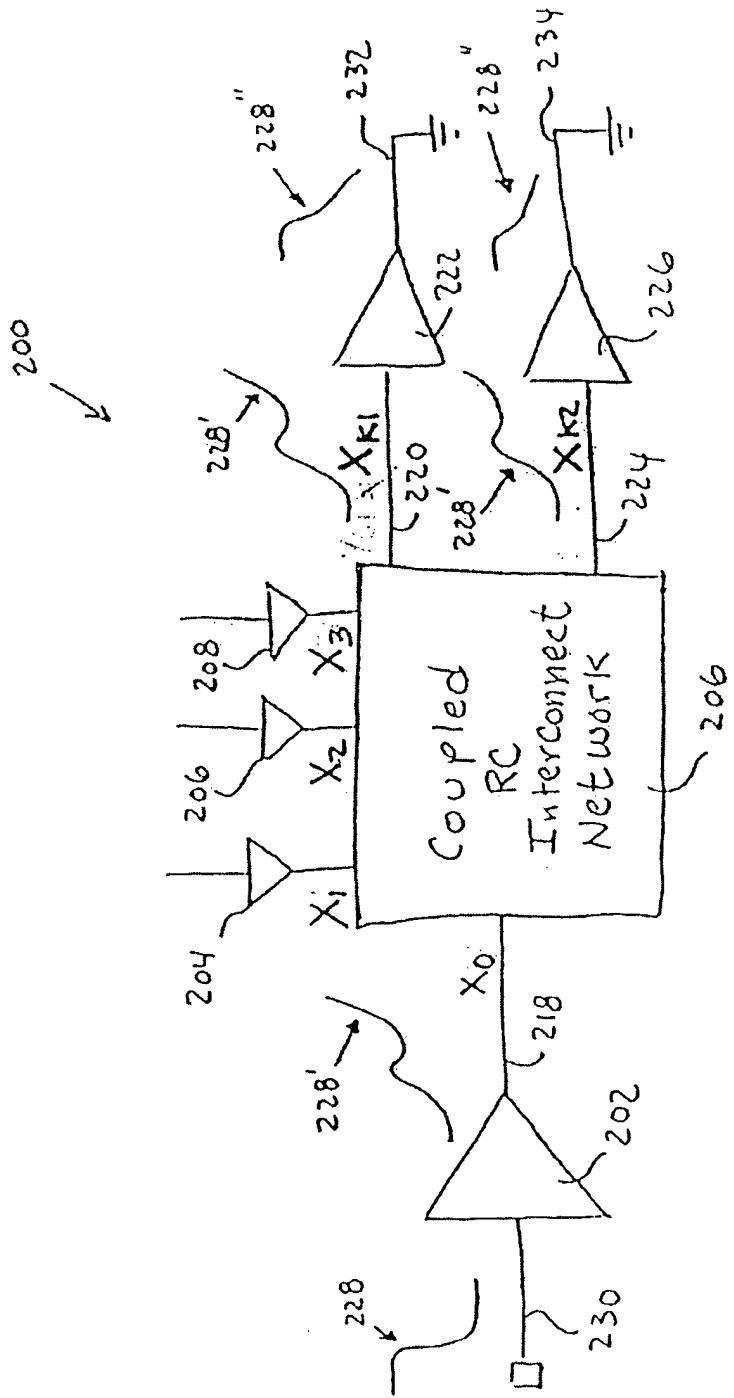


FIGURE 2

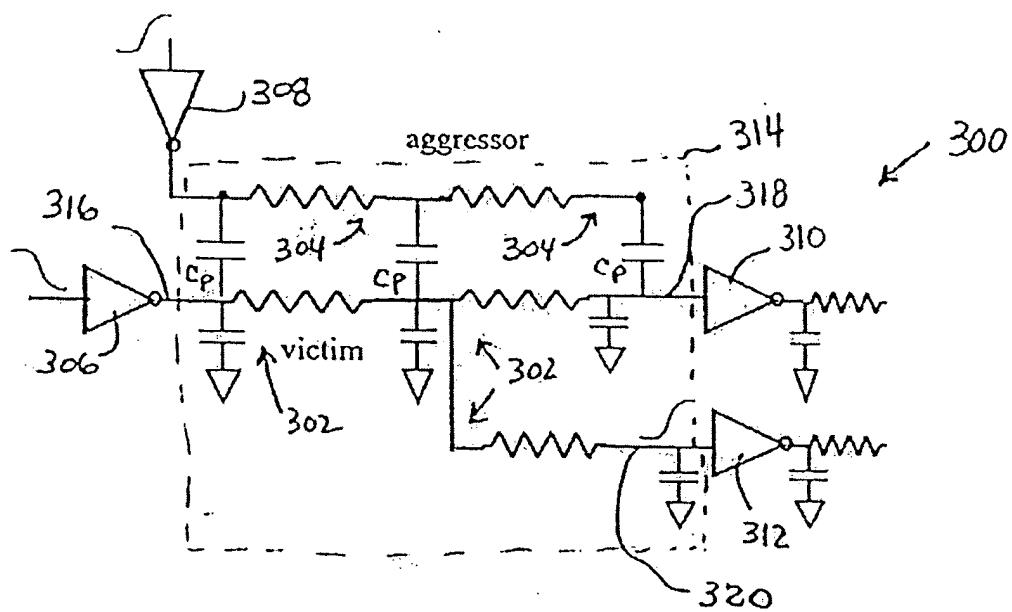


FIGURE 3

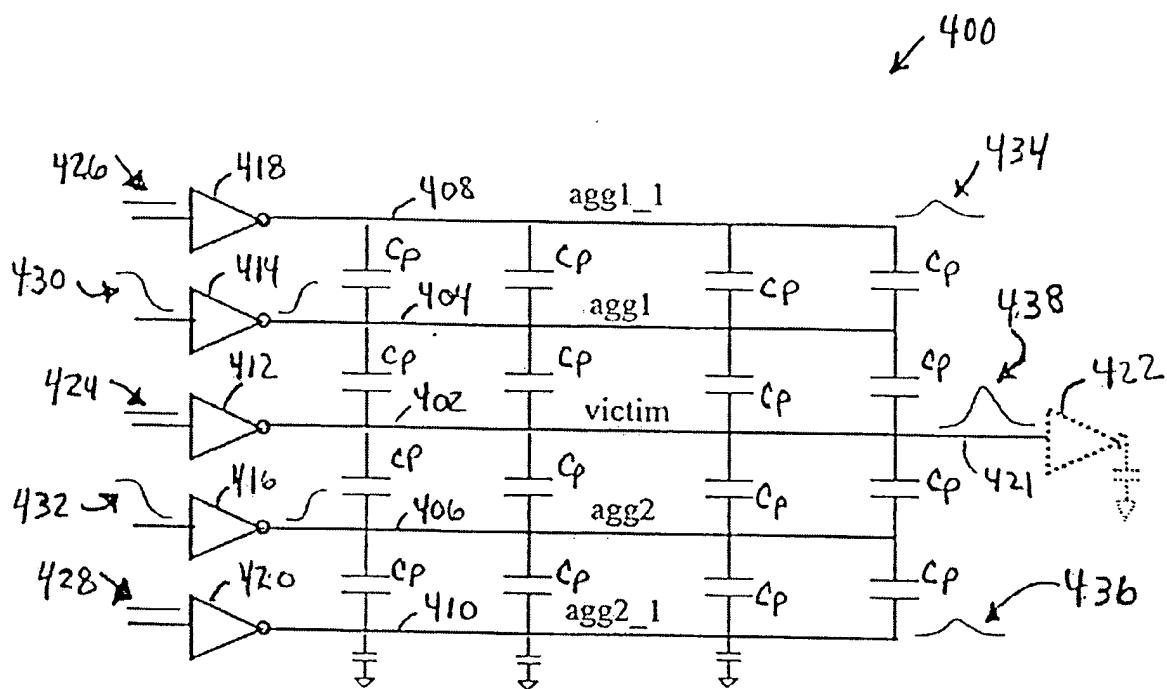


FIGURE 4

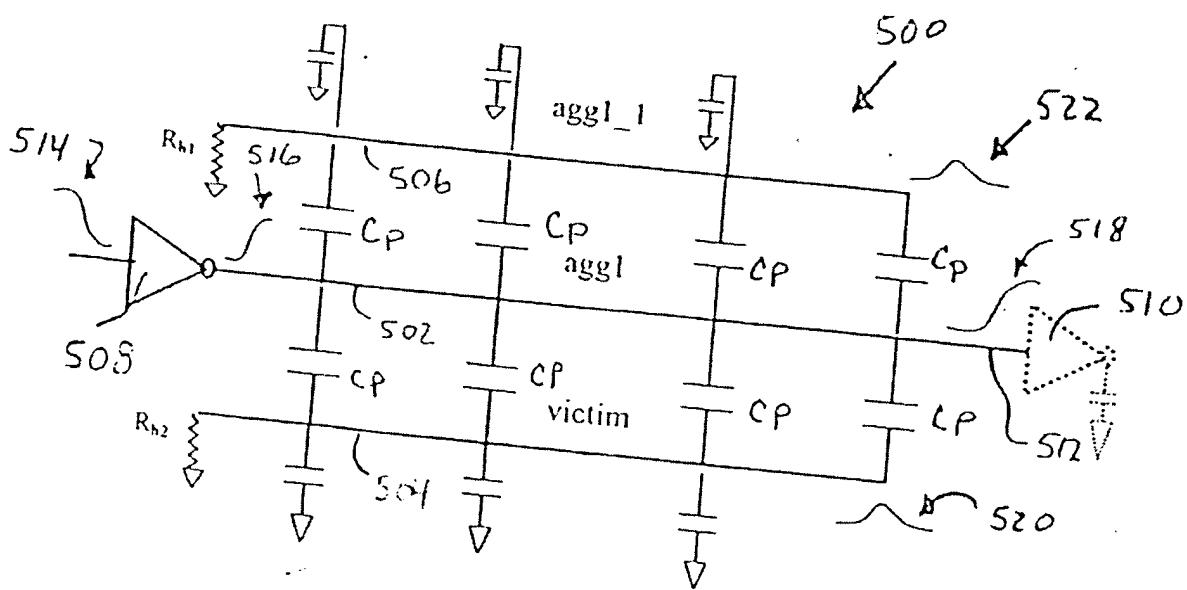


FIGURE 5

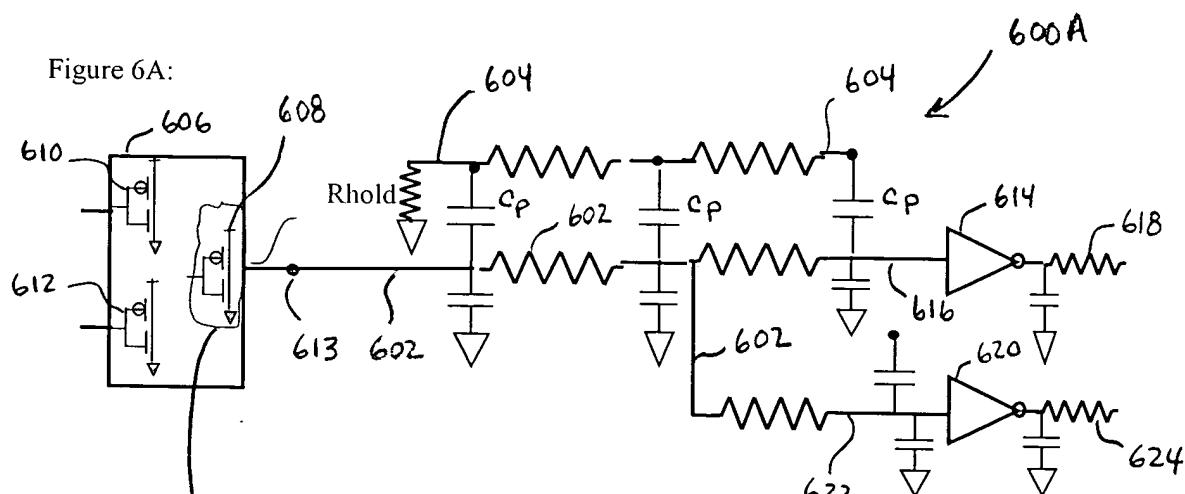
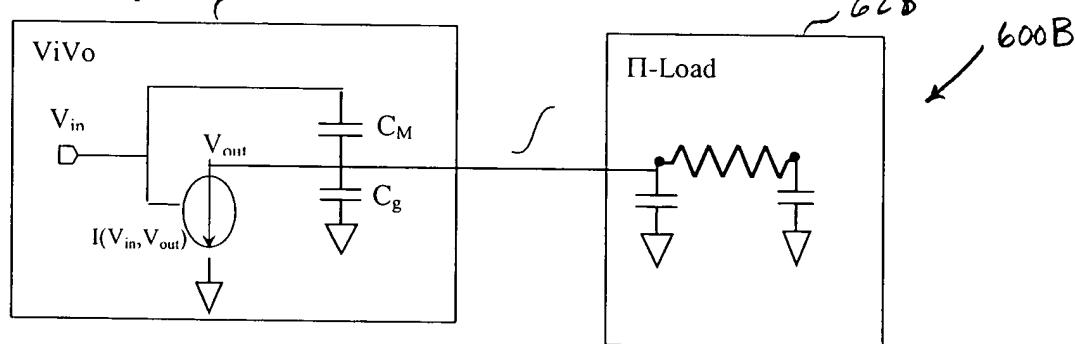


Figure 6B:



V_{in}/V_{out}	V_{o0}	V_{o1}	V_{o2}	...	V_{oN}
V_{i0}	I_{00}	I_{01}	I_{02}	...	I_{0N}
V_{i1}	I_{10}	I_{11}	I_{12}	---	I_{1N}
V_{i2}	I_{20}	I_{21}	I_{22}	---	I_{2N}
.
.
.
V_{iM}	I_{M0}	I_{M1}	I_{M2}	...	I_{MN}

FIGURE 7

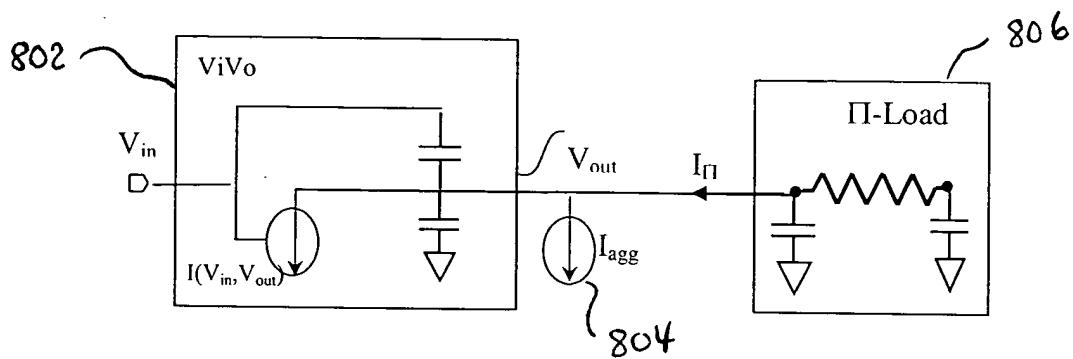


Figure 8

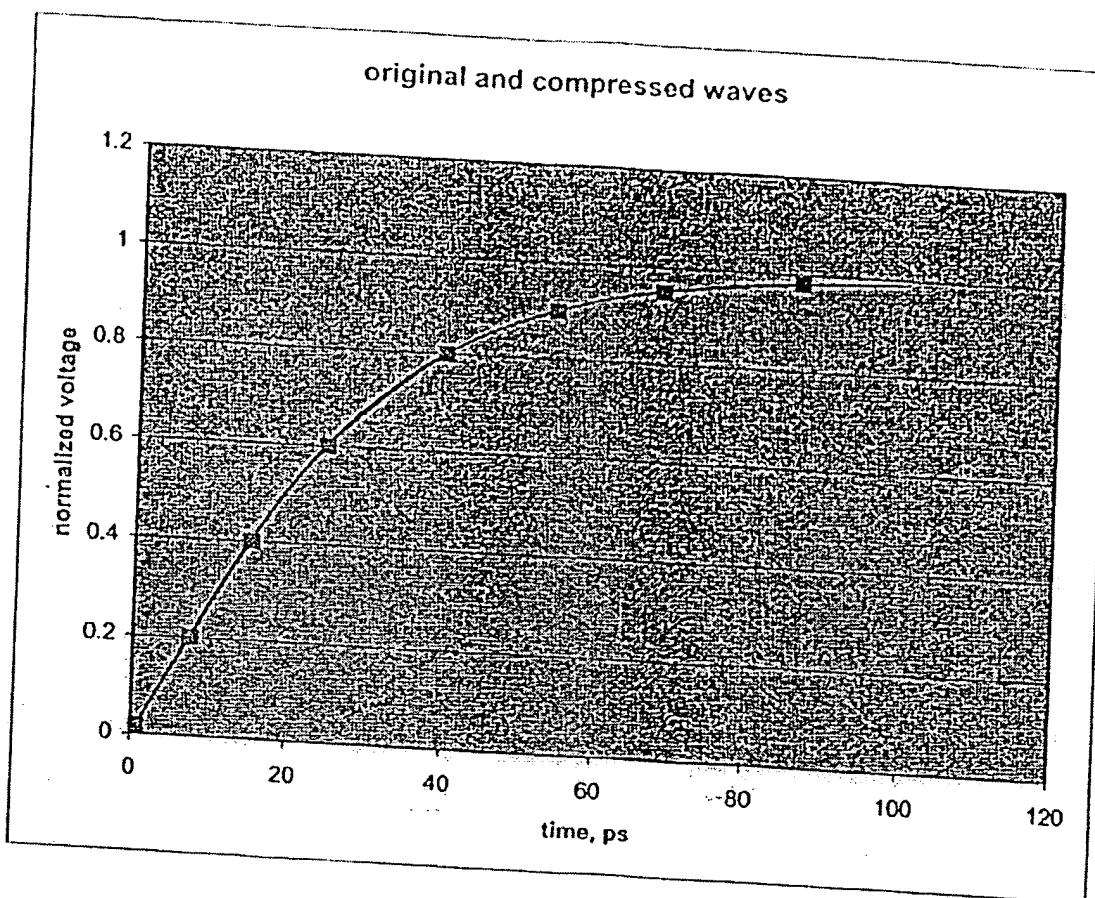


FIGURE 9

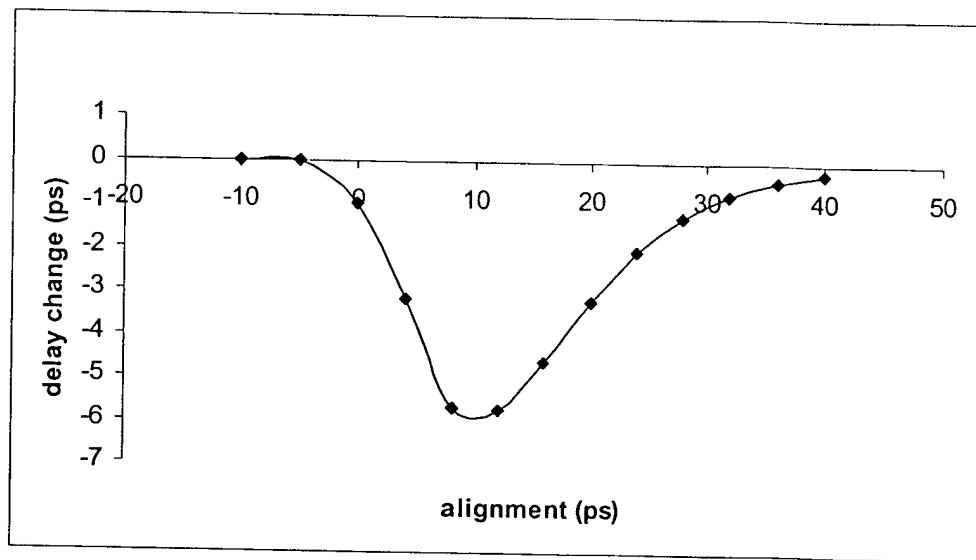


Figure 10

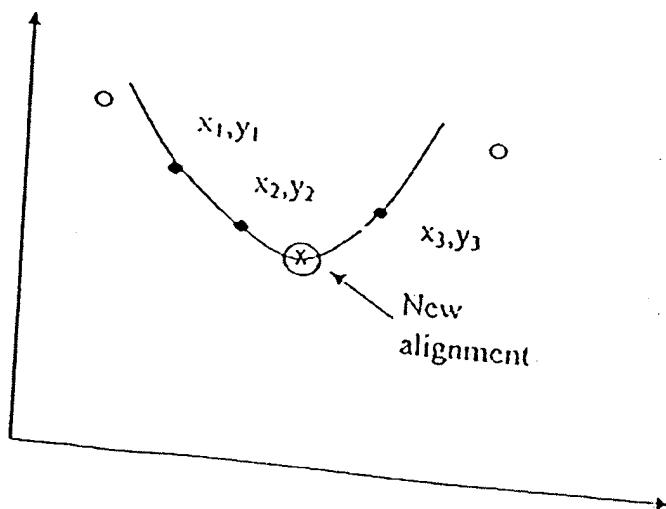


FIGURE 11

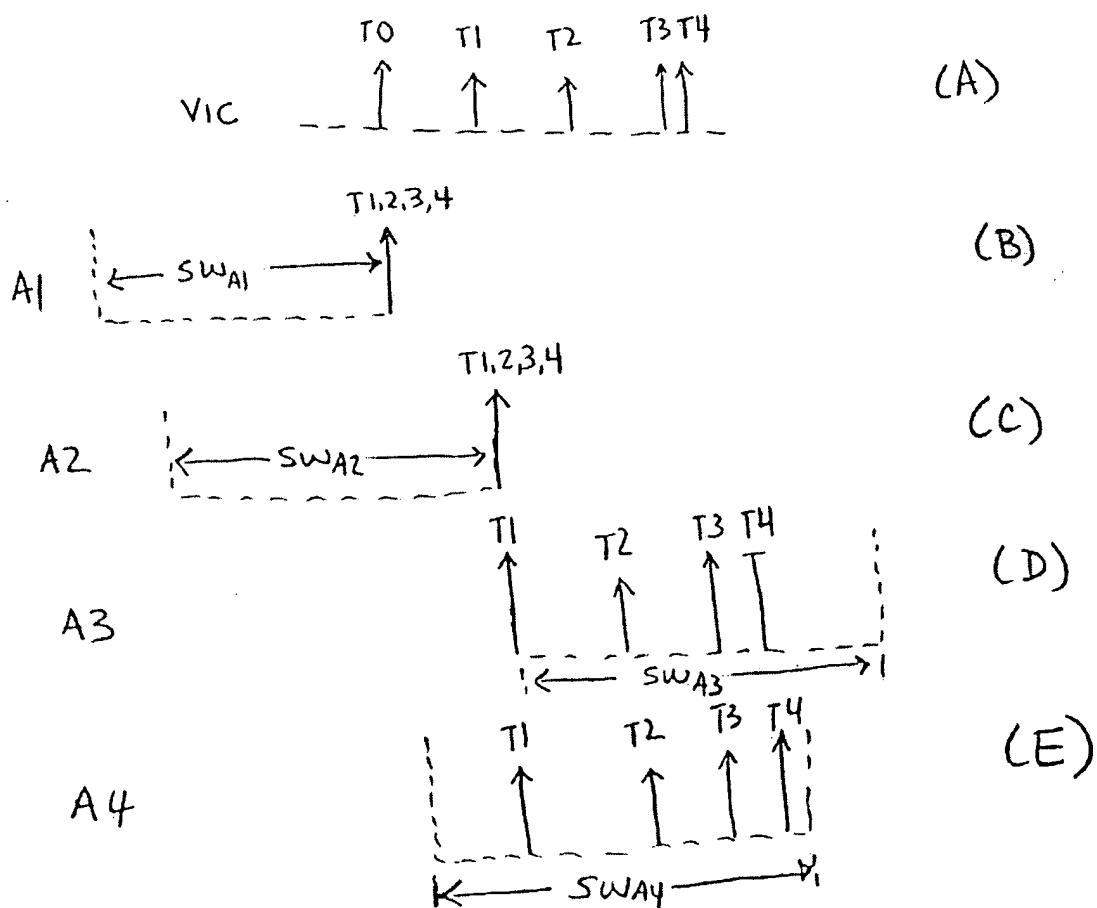


FIGURE 12

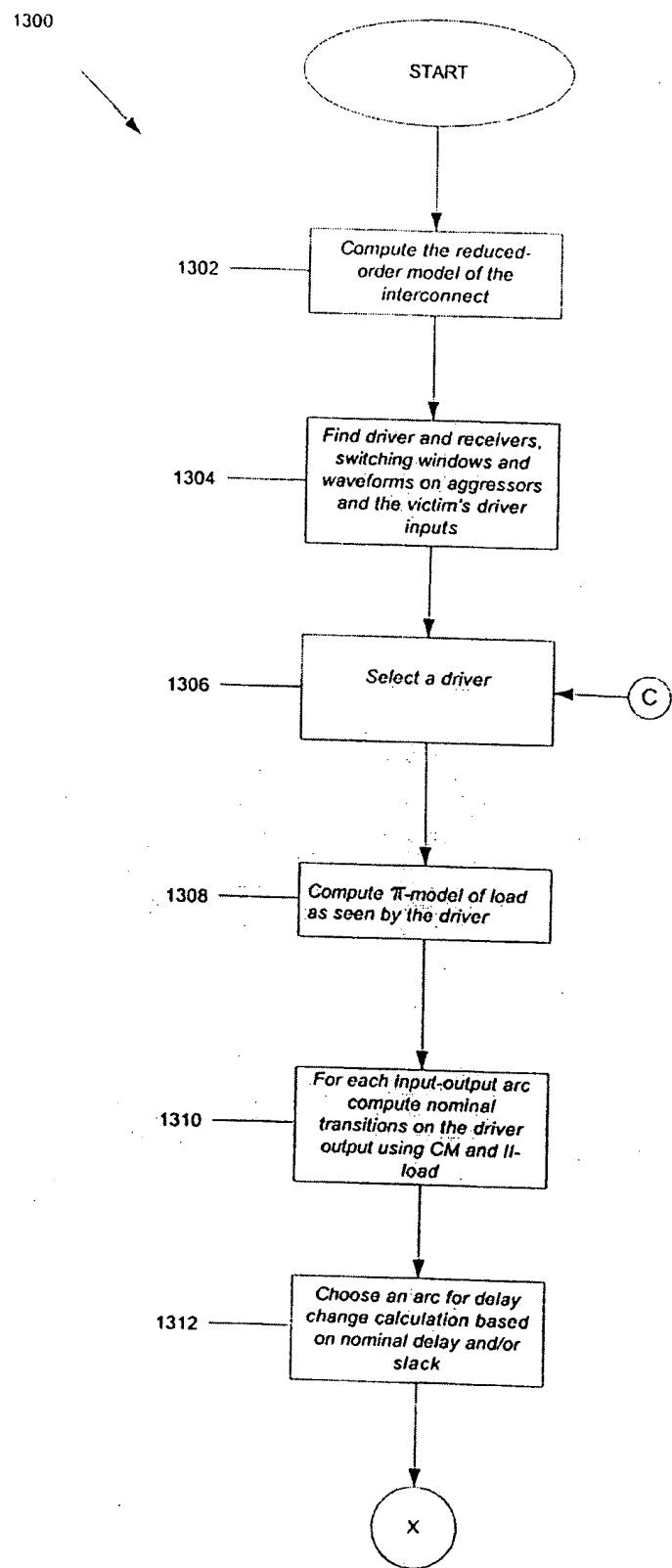


Figure 13A

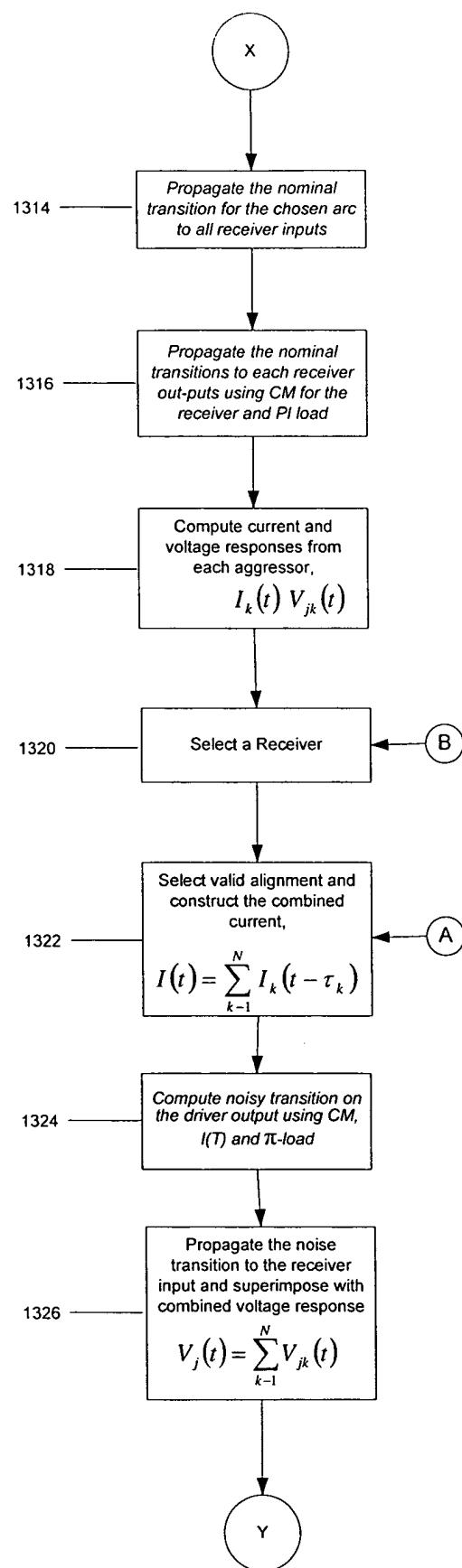


Figure 13B

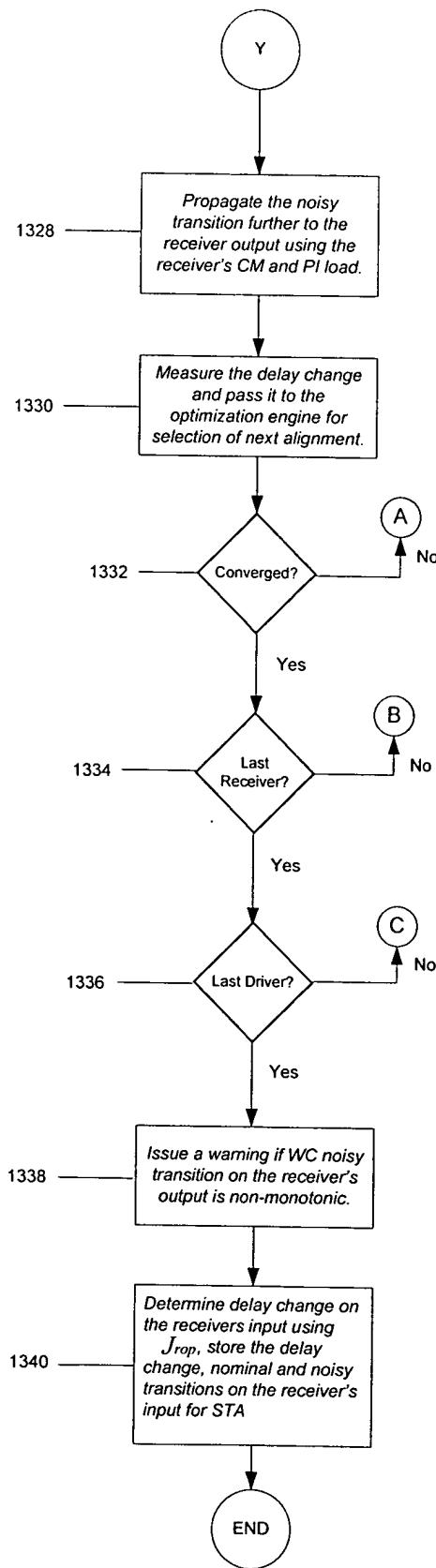


Figure 13C

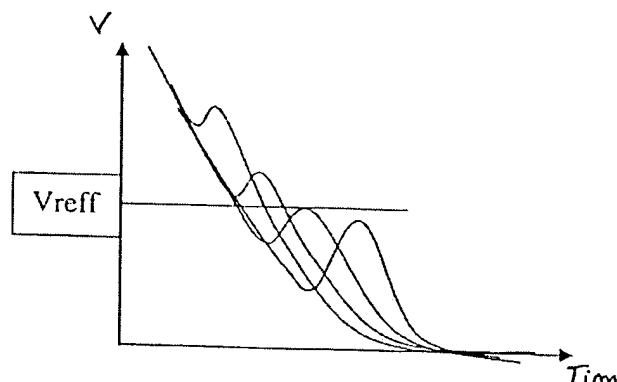


FIGURE 14A

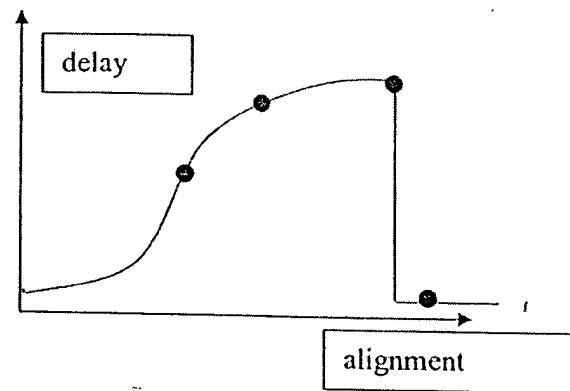


FIGURE 14B

Delay pushout comparison

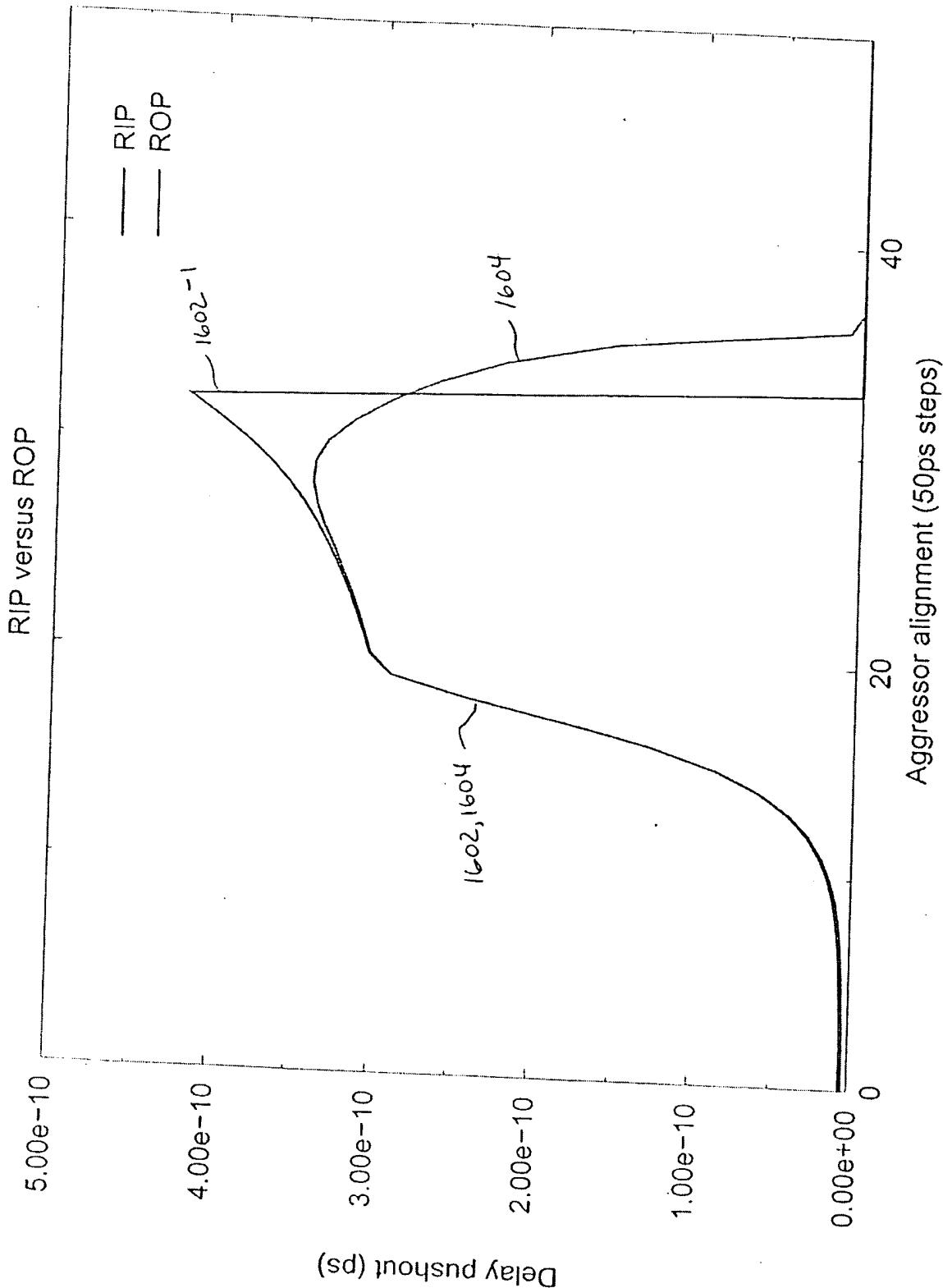


FIGURE 15

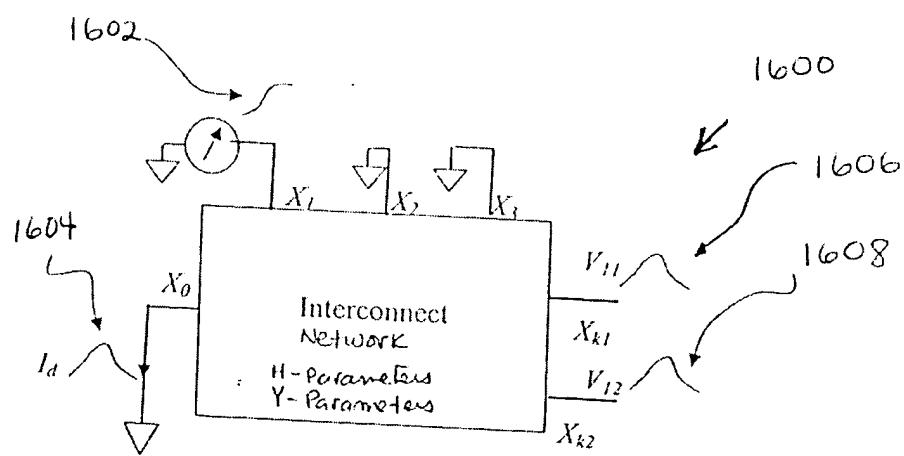


FIGURE 16